IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

WARREN M. FARNWORTH SALMAN AKRAM

e. Willia

3/IDS

Serial No.:

ART UNIT: 2858

Filing Date: April 27, 2001

EXAMINER: KORBERT, R.

Division of Serial No. 09/266,237 filed on March 10, 1999

For: TEST INTERCONNECT FOR BUMPED

SEMICONDUCTOR COMPONENTS AND

METHOD OF FABRICATION

Attorney Docket No. 97-1433.1

INFORMATION DISCLOSURE STATEMENT April 27, 2001

Assistant Commissioner for Patents BOX PATENT APPLICATION Washington, D.C. 20231

Sir:

The following sections are being submitted for this Information Disclosure Statement:

1.	<u>x</u>	Preliminary Statements.
2.	x	FORM PTO SB/08
3.		Statement As to Information Material to Examination Not Found in Patents or Publications.
4.	<u>x</u>	Identification of Prior Application in Which Listed Information Was Already Cited and For Which No Copie are Submitted Or Need be Submitted.
5.		Cumulative Patents or Publications.
6.		Copies of Listed Information Items Accompanying This Statement.
7.		Concise Explanation of Non-English Language Listed Information Items.
8.		Translation(s) of Non-English Language Documents.

9. <u>x</u> Identification of Person(s) Making This Information Disclosure Statement.

Section 1. Preliminary Statements

Applicants submit herewith patents, publications or other information of which they are aware which they believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. §1.56.

The filing of this information disclosure statement shall not be construed as a representation that a search has made (37 C.F.R. 1.56 (g)), an admission that the information cited is, or is considered to be, material to patentability or that no other material information exists.

Section 2. Identification of Person(s) Making This INFORMATION DISCLOSURE STATEMENT

(a) the inventor(s) who signs below	
SIGNATURE OF INVENTOR	
Name of Inventor	
(b) x the attorney who signs below on the basis of:	
the information supplied by the inventor(s)
an individual associated with the filing prosecution of this application (37 CFR 1	
\underline{x} the information in the attorney's file.	

Section 3. Identification of Prior Application In Which Listed Information Was Already Cited and For Which No Copies are Submitted or Needed Be Submitted.

This application relies, under 35 U.S.C. 120, on the earlier filing date of application Serial No. 09/266,237 filed on March 10, 1999 entitled "TEST INTERCONNECT FOR BUMPED SEMICONDUCTOR COMPONENTS AND METHOD OF FABRICATION". The attached list of references were submitted to and/or cited by the Office in the prior applications and, therefore, are not required to be provided in this application pursuant to 37 CFR §1.98(d).

DATED this 27th day of April, 2001.

Respectfully submitted:

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CERTIFICATE OF MAILING BY "EXPRESS MAIL"

Express Mail No. ET 203 403 199US Date of Deposit: April 27, 2001

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Address" Service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, BOX PATENT APPLICATION, Washington, D.C. 20231.

Stephen A. Oxatton, Attorney for Applicants